

990530

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of)

Patel, et al.)

Serial No. 09/981,130)

Filed: October 17, 2001)

) Group No. 2121

For: FAST, ITERATIVE SYSTEM AND
METHOD FOR EVALUATION A
MODULO OPERATION
WITHOUT USING DIVISIONINFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR § 1.97(c)

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Commissioner of Patents
Washington, D.C. 20231

Dear Sir:

Applicants, through their attorney, submit herewith references in accordance with 37 CFR § 1.56.

The citation of these references is not intended to constitute an admission that any reference provided is prior art to the invention of this application unless specifically designated as such.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231, on:

November 21, 2002

(Date of Deposit)

Daria D. Kasmedo

(Name of the Person Making Deposit)

(Signature)

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The filing of this document shall not be construed to mean that any search has been made or, that if made such search was complete or exhaustive, or that no other material information as defined in 37 CFR § 1.56 exists. A list of the references is set forth on Form PTO-1449, which is enclosed herewith in duplicate along with a copy of each cited reference. Applicants respectfully request that the Examiner return to Applicants the enclosed copy of the Form PTO-1449 indicating consideration of the references.

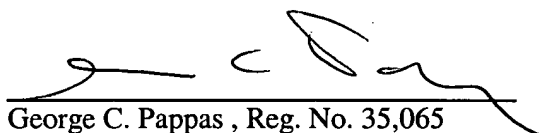
- ☒ The undersigned hereby certifies that each item of information contained in this Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. A copy of the International Search Report is also enclosed.

Respectfully submitted,

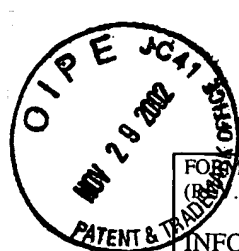
Dated:

1/21/02

By:


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FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE (Rev. 7-80) PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary) DATE MAILED: 11/21/02	ATTY. DOCKET NO. 990530	SERIAL NO. 09/981,130
	APPLICANT Patel, et al.	
	FILING DATE October 17, 2001	GROUP 2121

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	Ref No	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPRO- PRIATE
	A1						
	A2						
	A3						
	A4						
	A5						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	Ref No	DOCUMENT NUMBER	DATE	COUNTRY	NAME	CLASS	SUB CLASS
	B1						
	B2						

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OTHER PRIOR ART (Including Author, Title, Date, Pertinent Page, Etc.)

	C1	Parhami, Behrooz: "Analysis of Tabular Methods for Modular Reduction" Signals, Systems and Computers, 1994. USA, IEEE COMPUT. SOC. US. pgs. 526-530 (1994).
	C2	
EXAMINER		DATE CONSIDERED
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.		